

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## PATENT APPLICATION

Applicant : Seong-Hoon Lee

Application No.: 10/722,959 Confirmation No.: 9002

Filed : November 26, 2003

For : DIGITAL DELAY-LOCKED LOOP CIRCUITS

WITH HIERARCHICAL DELAY ADJUSTMENT

Group Art Unit : 2816

Examiner : An T. Luu

## EXPRESS MAIL CERTIFICATION

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Date of Deposit: April 12, 2005.

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## Enclosures:

- Request for Continued Examination (2 pp. in duplicate);
- 2. Supplemental Information Disclosure
  Statement (3 pp. in duplicate);
- Form PTO/SB/08A (2 pp. in duplicate);
- 4. Check in the Amount of \$790.00 (RCE Fee); and
- 5. Return Postcard.